


Assembly and Machine Language - Spring 1398 (2019) Midterm Exam	Instructor: B. Nasihatkon	 K. N. TOOSI UNIVERSITY OF TECHNOLOGY
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<p>Functions from the book</p> <table border="1" style="width: 100%;"> <tr> <td style="width: 30%;"><code>call print_int</code></td> <td>prints EAX as a signed integer</td> </tr> <tr> <td><code>call print_nl</code></td> <td>prints a newline character</td> </tr> </table> <p>Use 32-bit Netwide assembler code on a Linux machine.</p>	<code>call print_int</code>	prints EAX as a signed integer	<code>call print_nl</code>	prints a newline character	<p>Programming: Write programs in the designated code area as follows:</p> <table border="1" style="width: 100%;"> <thead> <tr> <th>label</th> <th>command</th> <th>arguments</th> </tr> </thead> <tbody> <tr> <td><code>loop1:</code></td> <td><code>call</code></td> <td><code>prog2</code></td> </tr> <tr> <td></td> <td><code>add</code></td> <td><code>eax, ebx</code></td> </tr> <tr> <td><code>prog2:</code></td> <td></td> <td></td> </tr> </tbody> </table>	label	command	arguments	<code>loop1:</code>	<code>call</code>	<code>prog2</code>		<code>add</code>	<code>eax, ebx</code>	<code>prog2:</code>		
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Question 1 (20 points) Update the values of the required registered after running each of the assembly instructions below. Notice that each instruction depends on the previous one. Write down the complete solutions for the signed cases.

command	AX (hex)	AL decimal (unsigned)	AL decimal (signed)	AH decimal (signed)
<code>mov ax, 0x12C8</code>	12C8	200	-56 C8h = 11001000 2C : 00111000=56	18 12h is positive 1*16+2 = 18
<code>shl ax, 3</code>	9640	64	64 40h is positive 4*16+0=64	-106 96h = 10010110 2C = 01101010=106
<code>sar ah, 2</code>	E540	64	64 unchanged	-27 E5h = 11100101 2C = 00011011 = 27
<code>ror ax, 1</code>	72A0	160	-96 A0h = 10100000 2C = 01100000 = 96	114 72h is positive 7*16+2=114
<code>add al, ah</code>	7212	18	18 12h is positive 1*16+2 = 18	114 unchanged

Question 2 (20 points)

What does the following code print? How the output relates to the input. What does each of the loops do? Explain each part of the code on the right-hand side. Assume that the input is positive.

<pre> call read_int mov ebx, eax mov esi, 0 mov ecx, 1 loop1: cmp ecx, ebx ja endloop1 mov eax, ebx mov edx, 0 div ecx cmp edx, 0 jnz notzero push ecx inc esi notzero: inc ecx jmp loop1 endloop1: mov eax, 0 mov ecx, esi loop2: pop ebx add eax, ebx loop loop2 call print_int call print_nl</pre>	<p>Let n be the input. The code reads an input n and prints the sum of its divisors. The first loop pushes the divisors on the stack. The second loop adds up the elements pushed on the stack.</p> <p>ESI = no_of_divisors = 0</p> <p>for ECX = 1...n {</p> <p> EDX = remainder = $n \% \text{ECX}$</p> <p> if (remainder = 0) {</p> <p> push ECX</p> <p> no_of_divisors += 1</p> <p> }</p> <p> ECX += 1</p> <p>}</p> <p>EAX = 0</p> <p>Repeat no_divisors times {</p> <p> EAX += stack.pop();</p> <p>}</p> <p>print EAX</p>
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Question 3 For each piece of assembly code in the left column, write a **single** equivalent assembly instruction. Disregard changes to the FLAGS registers. Explain your answer in the final column. (25 points)

	Single Instruction	Explanation
<pre>rol eax, 7 and eax, 0xFFFFFFFF80</pre>	<code>shl eax, 7</code>	Rotates the bits of EAX 7 bits to the left, then zeros out the lowest 7 bits.
<pre>jnc nocarry inc eax nocarry: add eax, ebx</pre>	<code>ADC eax, ebx</code>	$EAX = EAX + EBX + \text{CarryFlag}$
<pre>push edx mov edx, 0x80000000 and edx, eax shr eax, 1 or eax, edx pop edx</pre>	<code>SAR eax, 1</code>	Tests the sign bit of the EAX. Then shifts EAX to the right (fills with zero from left). If the sign bit of EAX was 1 in the first place, sets the last bit of the shifted EAX to 1.
<pre>push edx xor edx,edx mov dl, al shl edx, 24 shr eax, 8 or eax, edx pop edx</pre>	<code>ROR EAX, 8</code>	Saves the first 8 bits of EAX in DL. Shift EAX 8 bits to the right. Then sets the last 8 bits of EAX to what was saved in the DL.
<pre>push ebx push ecx push edx mov ecx, ebx mov edx, eax not ecx not edx and ebx, edx and eax, ecx or eax, ebx pop edx pop ecx pop ebx</pre>	<code>XOR EAX, EBX</code>	$A \text{ XOR } B = (\text{not}(A) \text{ and } B) \text{ OR } (A \text{ and not}(B))$

Question 4 We want to implement a function with a variable number of arguments. `int sum(int n, ...)`. The first argument `n` is always equal to the number of the remaining arguments. The function computes and returns the sum of the remaining arguments. For example `sum(3, 4, 7, 5)` returns 16, while `sum(3, 4, 7, 5, 8)` is invalid (we never perform such a call). The assembly code below consists of two files: `main.asm` and `sum.asm`. On the left (`main.asm`) write an assembly code which computes the sum of the registers `eax`, `ebx`, `ecx`, `edx`, `esi`, and `edi` by calling the function `sum`, and then prints it using the `print_int` function. On the right (`sum.asm`) write the body of the function `sum`. Assume that the first argument `n` is positive. **Observe all C declaration calling conventions.** Define the appropriate derivatives `global`, `extern` if needed. **(35 points)**

main.asm			sum.asm		
label	command	arguments			
	<code>segment</code>	<code>.text</code>			
<code>main:</code>			<code>sum:</code>	<code>push</code>	<code>ebp</code>
	<code>push</code>	<code>edi</code>		<code>mov</code>	<code>ebp, esp</code>
	<code>push</code>	<code>esi</code>			
	<code>push</code>	<code>edx</code>		<code>mov</code>	<code>eax, 0</code>
	<code>push</code>	<code>ecx</code>		<code>mov</code>	<code>edx, ebp</code>
	<code>push</code>	<code>ebx</code>		<code>add</code>	<code>edx, 12</code>
	<code>push</code>	<code>eax</code>		<code>mov</code>	<code>ecx, [ebp+8]</code>
	<code>push</code>	<code>6</code>	<code>loop1:</code>	<code>add</code>	<code>eax, [edx]</code>
	<code>call</code>	<code>sum</code>		<code>add</code>	<code>edx, 4</code>
	<code>add</code>	<code>esp, 28</code>		<code>loop</code>	<code>loop1</code>
	<code>call</code>	<code>print_int</code>			
	<code>call</code>	<code>print_nl</code>		<code>pop</code>	<code>ebp</code>
	<code>mov</code>	<code>ebx, 0</code>		<code>ret</code>	
	<code>mov</code>	<code>eax, 1</code>			
	<code>int</code>	<code>0x80</code>			